

現状報告

2012/8/23

京都ATLAS meeting

田代 拓也

oks-file作成

① 新register のclass を定義するfile

② 各FPGAの新register に値を配るfile

(①で定義したclassを使用)

↑
GUI (oks-data-editor) で編集

class 定義file

```
<attribute name="ScalerEifi20" description="trigger counter for eifi20" type="u32" format="hex"/>
<attribute name="ScalerEifi21" description="trigger counter for eifi21" type="u32" format="hex"/>
<attribute name="ScalerEifi30" description="trigger counter for eifi30" type="u32" format="hex"/>
<attribute name="ScalerEifi31" description="trigger counter for eifi31" type="u32" format="hex"/>
</class>

<class name="TGCSLPhase0">
<attribute name="CoincidenceFlag" description="prescale factor for NIM trigger output" type="u32" init-value="0" is-not-null="yes"/>
<attribute name="PTCoincidenceFlag" description="prescale factor for trigger output from sub-sector cluster 0" type="u32" init-value="0" is-not-null="yes"/>
<attribute name="ROIcoincidenceFlag0" description="prescale factor for trigger output from sub-sector cluster 1" type="u32" init-value="0" is-not-null="yes"/>
<attribute name="ROIcoincidenceFlag1" description="prescale factor for trigger output from sub-sector cluster 2" type="u32" init-value="0" is-not-null="yes"/>
<attribute name="ROIcoincidenceFlag2" description="prescale factor for trigger output from sub-sector cluster 3" type="u32" init-value="0" is-not-null="yes"/>
<attribute name="ROIcoincidenceFlag3" description="prescale factor for trigger output from sub-sector cluster 4" type="u32" init-value="0" is-not-null="yes"/>
<attribute name="ROIcoincidenceFlag4" description="prescale factor for trigger output from sub-sector cluster 5" type="u32" init-value="0" is-not-null="yes"/>
</class>
```

既存のfileに新しくclassを追加

classのmember (7つ)が新registerに対応する。

registerを配るfile

```
<attr name="CoincidenceFlag" type="u32">1</attr>
<attr name="PTCoincidenceFlag" type="u32">0xf0</attr>
<attr name="ROICoincidenceFlag0" type="u32">0xffffffff</attr>
<attr name="ROICoincidenceFlag1" type="u32">0xffffffff</attr>
<attr name="ROICoincidenceFlag2" type="u32">0xffffffff</attr>
<attr name="ROICoincidenceFlag3" type="u32">0</attr>
<attr name="ROICoincidenceFlag4" type="u32">0</attr>
</obj>

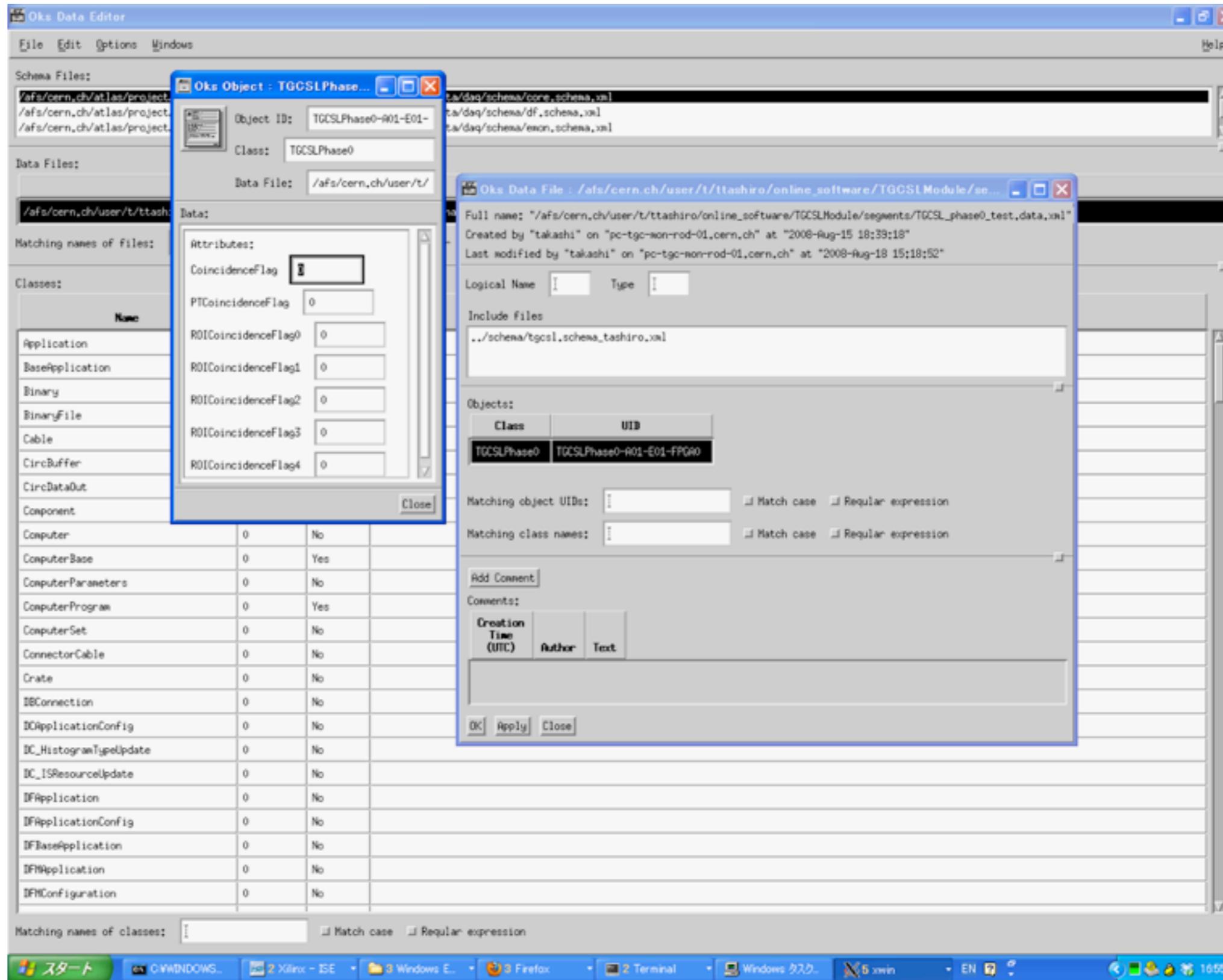
<obj class="TGCSLPhase0" id="TGCSLPhase0-A12-E24-FPGA1">
  <attr name="CoincidenceFlag" type="u1">1</attr>
  <attr name="PTCoincidenceFlag" type="u8">0xf0</attr>
  <attr name="ROICoincidenceFlag0" type="u32">0xffffffff</attr>
  <attr name="ROICoincidenceFlag1" type="u32">0xffffffff</attr>
  <attr name="ROICoincidenceFlag2" type="u32">0xffffffff</attr>
  <attr name="ROICoincidenceFlag3" type="u32">0</attr>
  <attr name="ROICoincidenceFlag4" type="u32">0</attr>
</obj>

<obj class="TGCSLPhase0" id="TGCSLPhase0-A12-F12-FPGA0">
  <attr name="CoincidenceFlag" type="u32">1</attr>
  <attr name="PTCoincidenceFlag" type="u32">0xf0</attr>
  <attr name="ROICoincidenceFlag0" type="u32">0xffffffff</attr>
  <attr name="ROICoincidenceFlag1" type="u32">0xffffffff</attr>
  <attr name="ROICoincidenceFlag2" type="u32">0xffffffff</attr>
  <attr name="ROICoincidenceFlag3" type="u32">0</attr>
  <attr name="ROICoincidenceFlag4" type="u32">0</attr>
</obj>
```

各FPGAに対し、代入する値を設定
今回は全FPGAに同じ値を設定

oks data editor

GUIでの編集



編集の反映の
確認はまだ
行っていない