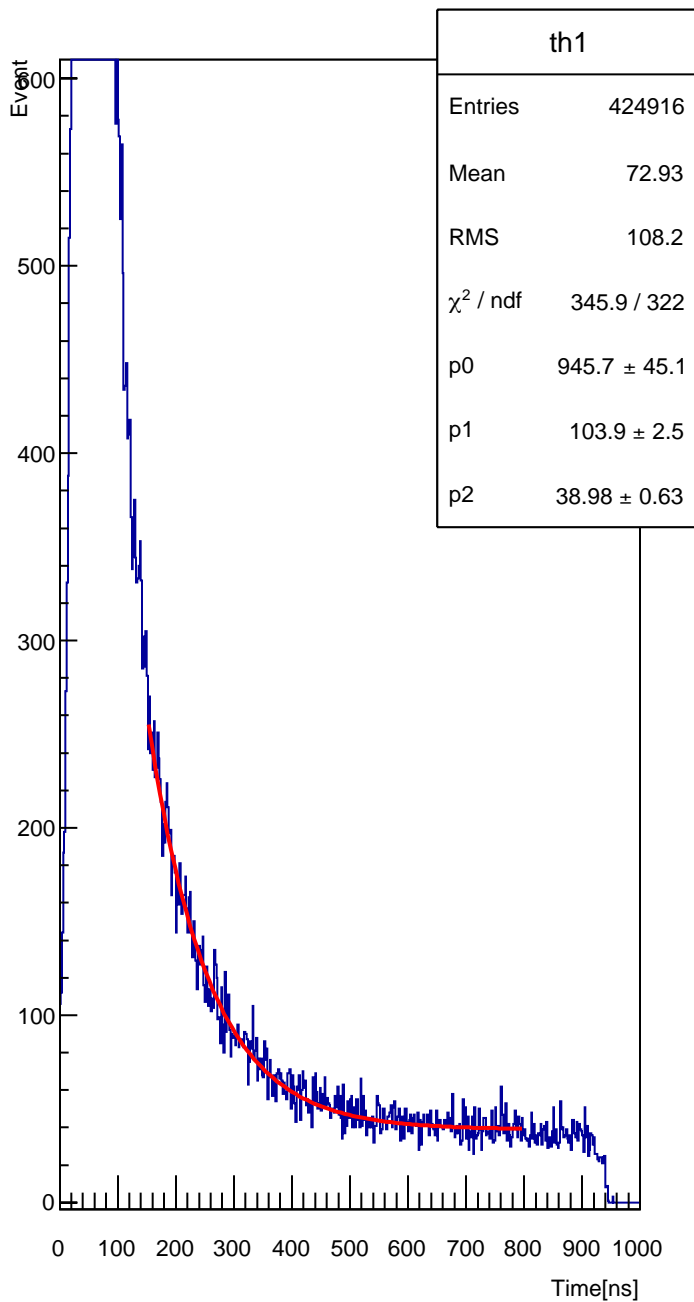


ADC1



ADC2

