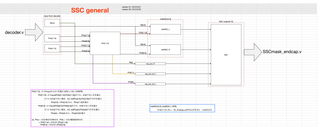
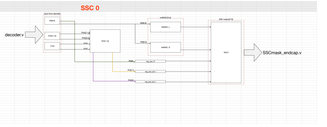
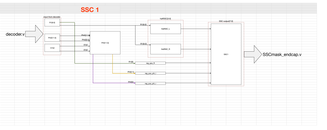
SSC general



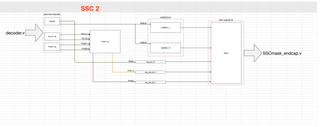
SSC 0



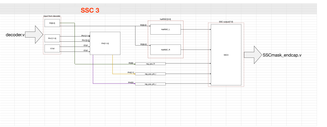
SSC 1



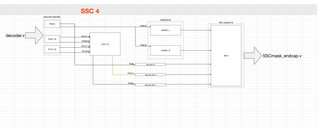
SSC 2



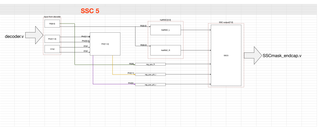
SSC 3



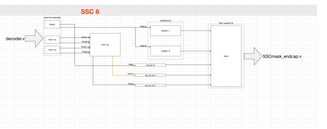
SSC 4



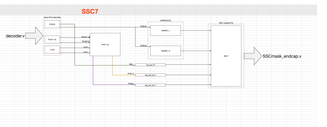
SSC 5



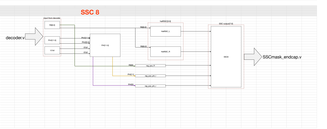
SSC 6



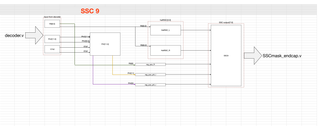
SSC7



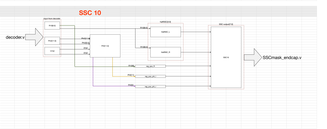
SSC 8



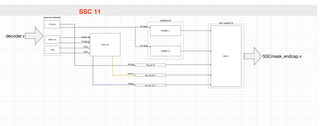
SSC 9



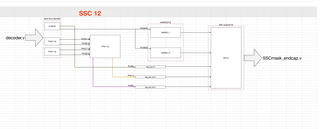
SSC 10



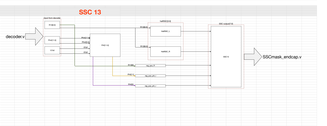
SSC 11



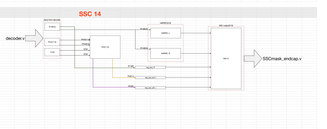
SSC 12



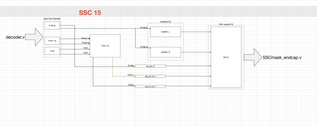
SSC 13



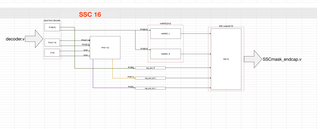
SSC 14



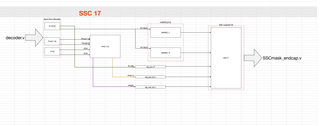
SSC 15



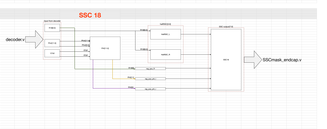
SSC 16



SSC 17



SSC 18



Input/Output Data

Input

R0~R18[6:0]

bit	
0	dR[0]
1	dR[1]
2	dR[2]
3	dR[3]
4	sign
5	H/L
6	pos

PHI0~3[11:0]

bit	
0	dφ[0]
1	dφ[1]
2	dφ[2]
3	sign
4	H/L
5	pos
6	dφ[0]
7	dφ[1]
8	dφ[2]
9	sign
10	H/L
11	pos

Output

SSC0~18[7:0]

bit	
0	SSC_Pt[0]
1	SSC_Pt[1]
2	SSC_Pt[2]
3	reg_pos_R
4	reg_pos_phi
5	whichSSC
6	detect_hit
7	SSC_sign